

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,370,564 B2
DATED : April 9, 2002
INVENTOR(S) : John Bretscher

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [22], delete "Feb. 12, 2001" and substitute -- April 6, 1998 -- in its place.

Column 14.

Line 9, delete "claim 13" and substitute -- claim 12 -- in its place.

Line 14, delete "claim 13" and substitute -- claim 12 -- in its place.

Signed and Sealed this

Twenty-second Day of October, 2002

Attest:

A handwritten signature in black ink, appearing to read "James E. Rogan", with a horizontal line drawn underneath it.

Attesting Officer

JAMES E. ROGAN
Director of the United States Patent and Trademark Office

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Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The Title page should be deleted and substitute therefore the attached Title page.

Drawings.

Please replace the drawing sheet which includes figure 6 with the new drawing sheet attached.

Column 8.

Lines 52 and 64, "108" should be -- 122 --.

Signed and Sealed this

Twenty-third Day of September, 2003

A handwritten signature in black ink, appearing to read "James E. Rogan", with a long horizontal line extending from the end of the signature.

JAMES E. ROGAN
Director of the United States Patent and Trademark Office

(12) **United States Patent**
Bretscher

(10) Patent No.: **US 6,370,564 B2**
(45) Date of Patent: **Apr. 9, 2002**

(54) **COMPUTER SYSTEM ARCHITECTURE AND METHOD FOR MULTI-USER, REAL-TIME APPLICATIONS**

(75) Inventor: **John Bretscher, Elgin, IL (US)**

(73) Assignee: **Ameritech, Services, Inc., Hoffman Estates, IL (US)**

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: **09/055,523**

(22) Filed: **Feb. 12, 2001**

Related U.S. Application Data

(62) Division of application No. 08/661,428, filed on Jun. 11, 1996, now Pat. No. 6,175,854.

(51) Int. Cl.⁷ **G06F 15/16**

(52) U.S. Cl. **709/205; 709/203; 709/219**

(58) Field of Search **709/205, 201, 709/203, 204, 218, 219, 220, 229, 214, 215; 345/330, 331; 370/264, 265, 267; 379/93.21, 205**

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(57) **ABSTRACT**

An architecture is disclosed for a computer system that runs applications serving multiple users. The computer system includes multiple processors, some of which run quick applications, i.e., requiring real time response, while others run applications with less stringent requirements. Each real time processor can be dedicated to running just one instance of an application. The processors can be of disparate types running disparate operating systems and optimized for disparate applications. The system is centrally controlled with the processors communicating among themselves over a shared LAN or via a communications switch. The system may also facilitate simultaneous voice and data communications among users. Users communicate with the system using any of a number of standard techniques; including dial-up telephone lines, ISDN, packet access services, ADSL, cable TV and the like.

15 Claims, 6 Drawing Sheets



